

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,018,363 B2
APPLICATION NO. : 09/765498
DATED : March 28, 2006
INVENTOR(S) : Cowan et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page

REFERENCES CITED

In Item (56), under "U.S. PATENT DOCUMENTS", in Column 1, Line 6, delete
"Atherton.....235/494" and
insert -- Atherton.....250/566 --, therefor.

IN THE DRAWINGS

In Fig. 14, Sheet 18 of 22, delete "0 04, 0 03, 0 02, 0 01" insert
-- 0.04, 0.03, 0.02, 0.01 --, therefor.

In Fig. 14, Sheet 18 of 22, delete "0 96, 0 7, 0 8" insert -- 0.96, 0.7, 0.8 --, therefor.

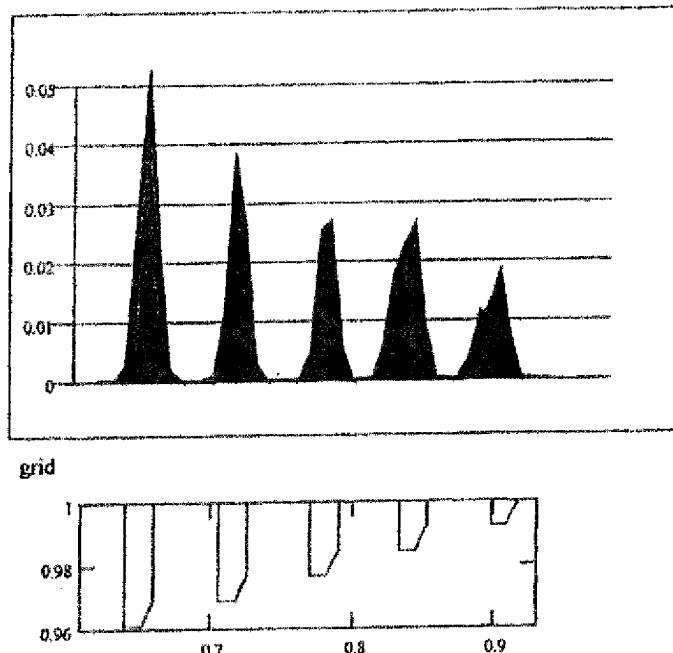


Figure 14

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,018,363 B2
APPLICATION NO. : 09/765498
DATED : March 28, 2006
INVENTOR(S) : Cowan et al.

Page 2 of 2

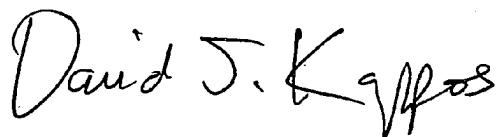
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 3, Line 48, after "material" insert -- . --.
In Column 6, Line 19, delete "illustrate s" and insert -- illustrates --, therefor.
In Column 7, Line 58, delete "therethough" and insert -- therethrough --, therefor.

In Column 18, Line 23, in Claim 18, after "wall" insert -- , --.

Signed and Sealed this

Eighth Day of December, 2009



David J. Kappos
Director of the United States Patent and Trademark Office